Docket No.: 5926P048

IAP20 Rec'o POLIFIO LIGIUM 2006 IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the	App.	lication	of:
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KLAUS DIETER MCDONALD-MAIER, ET AL.

Application No.:

Filed:

For: Integrated Circuit with Debug Support

Interface

Art Group:

Examiner:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed), which are being submitted concurrently with the Utility Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

10/5834

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Date:

Respectfully submitted,

BLAKELY, SOKOLOFF TAYLOR & ZAFMAN LLP

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AP20 Rec'd PCT/PTO 16 JUN 2006

Substitute for form 1449A/PTO		Complete if Known			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			SUDE	Application Number	40/202405
			JOURE	Filing Date	10/282492
			CANT	First Named Inventor	Klaus Dieter McDonald-Maier
				Art Unit	
				Examiner Name	
Sheet	1	of	1	Attorney Docket Number	5926P048

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²	
		"Debug Support for Complex System-on-Chips", A. Mayer, et al., Embedded Systems Conference 2003, San Francisco, XP008060789, 16 pages		
		"On-Chip Debug Support For Embedded Systems-On-chip", K.D. Maier, 2003 IEEE,V-565-V568		
		"High Electronic-Optical Conversion Efficiency in a Vertical to Surface Transmission Electro-Photonic Device with a Vertical Cavity", K. Kurihara, et al., August 1992, pgs 598-600		

Examiner		Date	
Signature	•	Considered	

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wir) 08/11/2003. Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

^{*}Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.